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P21407.A03

IN THE LINITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Y. HANADA et al.

MAY 1 5 2002

Appl. No.

10/020,294

Filed

December 18, 2001

For

AUXILIARY POWER SUPPLY UNIT AND PORTABLE

ELECTRONIC SYSTEM

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents Washington, D.C. 20231

RECEIVED

JUN 1 9 2002

Group Art Unit: 2851

Sir:

Technology Center 2600

In accordance with the duty of disclosure under 37 C.F.R. 1.56, 1.97 and 1.98

Applicants hereby call the following co-pending, commonly assigned U.S. patent application to the Examiner's attention:

U.S. Patent Application No. 09/824,249 to HANADA, filed April 3, 2001, and entitled "A Power-Supply Unit Incorporating an Electric Double Layer Capacitor, and a Method of Charging the Electric Double Layer Capacitor".

Pursuant to 37 C.F.R. §1.98(a)(2), a copy of the above-noted patent application, as filed, is attached hereto. The Examiner is respectfully requested to review this application as well as any references cited therein.

Applicants note that the above-identified application was mis-identified in the Information Disclosure Statement filed March 18, 2002. In the Information Disclosure Statement filed March 18, 2002, the above-noted application was cited with the application

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ELECTRONIC SYSTEM

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

TC 2800 MAIL ROOM In accordance with the duty of disclosure under 37 C.F.R. 1.56, 1.97 and 1.98,

Applicants hereby call the following co-pending, commonly assigned U.S. patent applications to the Examiner's attention:

U.S. Patent Application No. 09/828,940 to HANADA et al., filed April 10, 2001, and entitled "Power Source Circuit and Electronic Still Camera";

U.S. Patent Application No. 10/020,294 to HANADA, filed December 18, 2001, and entitled "A Power-Supply Unit Incorporating an Electric Double Layer Capacitor, and a Method of Charging the Electric Double Layer Capacitor".

Pursuant to 37 C.F.R. §1.98(a)(2), a copy of each of the above-noted patent applications, as filed, is attached hereto. The Examiner is respectfully requested to review these applications as well as any references cited therein.